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Robust Query Processing in Co-Processor-accelerated Databases

co-processor and its host system [11] We illustrate this problem in Figure 1 for a GPU-based co-processor, which we use as a poster child in the context of this work We obtained this figure by executing Query 33 from the Star Schema Benchmark (a) on a commodity CPU; (b) using a GPU accelerator, assuming a cold-cache scenario (ie,

Accelerating Compute-Intensive Applications with GPUs and ...

ware/hardware co-design are becoming increasingly popular means to assist general purpose processors in performing complex and intensive computations on accelerator hardware GPUs and FPGAs, together with other accelerators such the vector processors of IBM's Cell [11], DSPs (digital signal processors), media processors and network processors

Accelerators

Accelerator vs co-processor A co-processor executes instructions Instructions are dispatched by the CPU An accelerator appears as a device on the bus The accelerator is controlled by registers

Introduction to GPUs

Accelerator-based Systems CPUs have always depended on co-processors - I/O co-processors to handle slow I/O - Math co-processors to speed up computation These have mostly been transparent - Drop in the co-processor and everything sped up The GPU is not a transparent accelerator for general purpose computations

Parallel Data Mining on Graphics Processors

The GPU is an integral component in commodity machines It was previously designed to be a co-processor to the CPU for games and other interactive applications Recently, the GPU has been used as a hardware accelerator for various non-graphics applications, such as scientific computation, matrix multiplication [26],

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NVIDIA Accelerators for HPE

NVIDIA® Quadro™ Graphical Processing Unit (GPU) technology The NVIDIA accelerators enable seamless integration of GPU computing with HPE servers for high-performance computing, large data center graphics and virtual desktop deployments

How to Understand and Evaluate Deep Learning Processors

n CPU and GPU Platforms n Specialized / Domain Specific Hardware (ASICs) o Efficient Dataflows o Algorithm (DNN Model) and Hardware Co-Design o Flexibility and Scalability n Other Platforms o Processing In Memory / In Memory Computing o Field Programmable Gate Arrays (FPGAs) o Tools for Systematic Evaluation of DL Processors

Co-Processor Acceleration of an Unmodified Parallel Solid ...

co-processor and language are of subordinated relevance because productivity reasons limit the amount of code that may be reimplemented for acceleration Most important are the abstraction from the particular co-processor hardware (such that changes of co-processor and parallel language become manageable) and a global computation

1 Designing an FPGA-Accelerated Homomorphic Encryption ...

1 Designing an FPGA-Accelerated Homomorphic Encryption Co-Processor David Bruce Cousins, Kurt Rohloff, Daniel Sumorok Abstract—In this paper we report on our advances designing and implementing an FPGA-based computation accelerator as part of a Homomorphic Encryption Processing Unit (HEPU) co-processor