Domain Specific Processors Systems Architectures Modeling And Simulation Signal Processing And Communications

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Domain-specific architectures, as the name implies, are tailored to a class of workloads that share a common characteristic The devices are programmable, not hardwired as are tradi-tional ASICs Domain-specific architectures integrate application and deployment-aware functions and

support domain -specific languages for ease of use

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domain-specific processors A well known example are the programmable digital signal processors (DSPs) DSPs are developed for wireless communication systems (mostly driven by cellular standards) In a first generation this meant that DSPs were adapted to execute many types of filters (eg FIR, IRR), later communication algorithms such as Viterbi

ASAP2019-CFP-V2

Apr 02, 2019 · Application-Specific Systems, Architectures And Processors Cornell Tech, New York, Usa, July 15-17, 2019 Overview The 30th IEEE International Conference on Application-specific Systems, Architectures and Processors will take place in New York, US The 2019 edition of the conference is organized by Cornell Tech

Reconfigurable Interconnect for next generation systems

Therefore, these systems will consist of a heterogeneous set of domain specific processors and reconfigurable components The domain specific processors are introduced in section 3 Tuning the components of an architecture to the application domain, is an exploration of a design space, which we call the reconfiguration hierarchy

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Only performance path left is Domain Specific Architectures (DSAs) -Just do a few tasks, but extremely well Achieve higher efficiency by tailoring the architecture to characteristics of the domain Not one application, but a domain of applications Different from strict ASIC since still runs software 28

Models and Architectures for Heterogeneous System Design

• From formal dataflow models to MPCSoC architectures • Genetic algorithms + host-compiled models [T-HiPEAC'11] – Hierarchy of layered models for successive design space pruning

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domain specific flexibility can be expressed as a domain specific abstraction pyramid as shown for Networking, Video, and Signal Processing on Fig 1 In case of Video, the engine will consist of elements expressed in the Video pyramid, for example dedicated co-processors The SOC is connected together at the top level by a su-

What is Computer Architecture?

• Also application-domain specific processors • Implement critical domain-specific functionality in hardware • Examples: video encoding, 3D graphics • General rules - Hardware is less flexible than software + Hardware more effective (speed, power, cost) than software + Domain specific more "parallel" than general purpose

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on-chip multi-processors is intro-duced It consists of a library of soft macros (switches, network interfaces and links) that are design-time com-posable and tunable so that domain-specific heterogeneous architectures can be instantiated and synthesized Links can be pipelined with a flexible number of stages to decouple link

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