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B 3 A 0 A 1 B 2 B 1 B 0 B 3 B 2 B 1 B 0 B 3 B 2 B 1 B 0 0 Addend Augend Augend Sum and output carry 4-bit adder A2 Addend Sum and output carry 4-bit adder C 6 C 5 C 4 C 3 C 2 C 1 C 0 Fig 4-16 4-Bit ...

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Laboratory Exercise #11 A Simple ... - Texas A&M University

31 Digital Combination-Lock In lab this week, we will design a digital combination-lock that can be used on a safe or access-controlled doorway To do so, we will require a few components shown in Figure ...

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